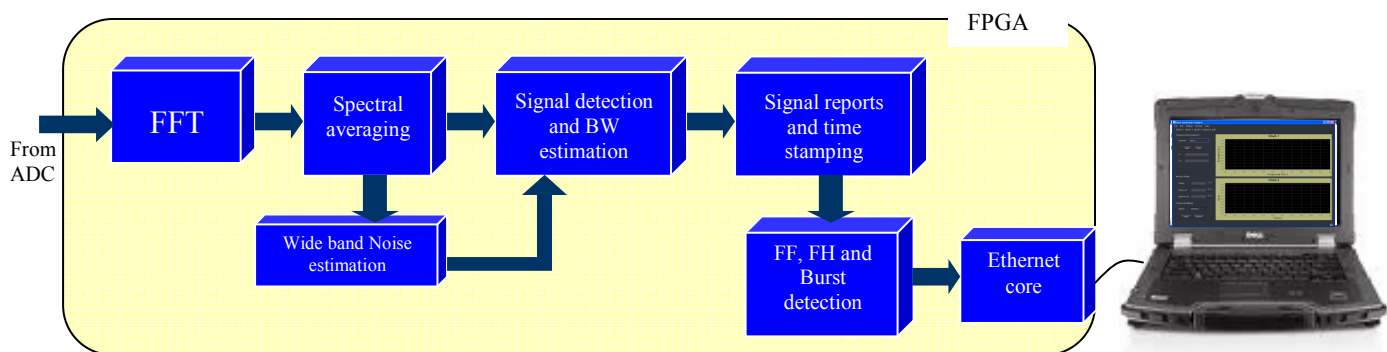


Wide Band Signal Interception and Reporting (WBSIR)

Release 4.0

WBSIR core enables the user to build complete FPGA based solution for signal interception applications over wide frequency bands. The cores provide complete solution with several reconfigurable options targeting for military and civilian spectrum vigilance applications.



- ◆ Complete FPGA based solution, resulting in high speed spectrum computation and report generation
- ◆ GUI with spectrum and waterfall display
- ◆ Detection of Fixed Frequency(FF), frequency hopping(FHSS) and Burst signal detection
- ◆ Different types of signal detection algorithms
- ◆ Spectral zoom with dynamically reconfigurable FFT core.
- ◆ Configurable Digital filters for providing output to narrow band monitoring receiver
- ◆ Verified on Virtex-II, Virtex-4 and Virtex-5 Xilinx FPGAs at the target speeds
- ◆ Processing speeds supporting up to 125 Msps ADC (Full pipelined processing without dropping any sample data) on Virtex-5 FPGA
- ◆ Spectral activity and signal reports transmitted over Ethernet to PC.
- ◆ Can be integrated with other systems using APIs provided Ethernet communication
- ◆ Customized core development as per the user requirements
- ◆ Optional GPS receiver synchronized time stamping
- ◆ Feature rich GUI providing waterfall and signal activity display on PC
 - Options to enter search bands and lock out bands
 - Options to change the spectral zoom factor
 - Options to select among various algorithms for detection and parameter extraction
- ◆ Flexible licensing options : (a) Synthesized netlists and wrapper VHDL (ordering code: SDR-WBSIR-04-SYN)
(b) Full VHDL source code (ordering code: SDR-WBSIR-04-HDL)

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